AMENDMENT

To the Claims:

1-7 (canceled)

8. (new) A pixel array, comprising:

M*N pixels, each row of the pixels having a plurality of pixel sets, wherein

the jth and the $(j+1)^{th}$ pixel sets of the ith row of the pixels substantially have different driving polarity, where i and j are positive integers;

the jth and the $(j+1)^{th}$ pixel sets of the $(i+1)^{th}$ row of the pixels substantially have different driving polarity;

the jth pixel set of the ith row of the pixels and the jth pixel set of the $(i+1)^{th}$ row of the pixels substantially have different driving polarity; and

the $(j+1)^{th}$ pixel set of the ith row of the pixels and the $(j+1)^{th}$ pixel set of the $(i+1)^{th}$ row of the pixels substantially have different driving polarity;

a plurality of data lines for respectively providing a corresponding pixel voltage, wherein the polarity of the pixel voltage provided by the kth data line is opposite to the polarity of the pixel voltage provided by the $(k+1)^{th}$ data line, where k is a positive integer; and

a plurality of gate lines, wherein the rth gate line is used for turning on all odd pixels in the jth and the $(j+1)^{th}$ pixel sets of the ith row of the pixels and all even pixels in the jth and the $(j+1)^{th}$ pixel sets of the $(i+1)^{th}$ row of the pixels, where r is a positive integer.

9. (new) The pixel array according to claim 8, wherein each pixel set comprises 3 pixels or a multiple of 3 pixels.

10. (new) A pixel array, comprising:

M*N pixels, each row of the pixels having a plurality of pixel sets, wherein

all of the pixel sets in the ith row of the pixels substantially have same driving polarity, where i is a positive integer;

all of the pixel sets in the $(i+1)^{th}$ row of the pixels substantially have same driving polarity; and

all of the pixel sets in the ith row of the pixels and all of the pixel sets in the $(i+1)^{th}$ row of the pixels substantially have different driving polarity;

a plurality of data lines for respectively providing a corresponding pixel voltage, wherein the polarity of the pixel voltage provided by the kth data line is opposite to the polarity of the pixel voltage provided by the $(k+1)^{th}$ data line, where k is a positive integer; and

a plurality of gate lines, wherein the rth gate line is used for turning on all odd pixels in each pixel set of the ith row of the pixels and all even pixels in each pixel set of the $(i+1)^{th}$ row of the pixels, where r is a positive integer.

11. (new) The pixel array according to claim 10, wherein each pixel set comprises 2 pixels or a multiple of 2 pixels.